

Attorney Docket No.: AM3299/T29900
AMAT No.: 3299
TTC No.: 16301-029900

PATENT APPLICATION
INTEGRATION SCHEME USING SELF-PLANARIZED
DIELECTRIC LAYER FOR SHALLOW TRENCH ISOLATION

Inventors:

Frederic Gaillard
A Citizen of France,
Rue des Tallifardieres
38500 Voiron
FRANCE

Fabrice Geiger
A Citizen of France,
29 avenue de la Plaine Fleurie
38240 Meylan
FRANCE

Ellie Yieh
A Citizen of the United States
5888 Pistoria Way
San Jose, CA 95138

Assignee:

APPLIED MATERIALS, INC.
P.O. Box 450A
Santa Clara, California 95052
A Delaware corporation

Status: Large Entity

TOWNSEND and TOWNSEND and CREW LLP
Two Embarcadero Center, 8th Floor
San Francisco, California 94111-3834
Tel: 303-571-4000

Inventor Information

Inventor One Given Name::	Frederic
Family Name::	GAILLARD
Postal Address Line One::	Rue des Tallifardieres
City::	Voiron
State or Province::	
Postal or Zip Code::	F-38500
Citizenship Country::	France
Inventor Two Given Name::	Fabrice
Family Name::	GEIGER
Postal Address Line One::	29 Avenue de la Pleine Fleurie
City::	Meylan
State or Province::	
Postal or Zip Code::	F-38240
Citizenship Country::	France
Inventor Three Given Name::	Ellie
Family Name::	YIEH
Postal Address Line One::	5888 Pistoria Way
City::	San Jose
State or Province::	California
Postal or Zip Code::	95138
Citizenship Country::	United States of America

Correspondence Information

Correspondence Customer Number:: 20350

Application Information

Title Line One::	INTEGRATION SCHEME USING SELF-PLANARIZED
Title Line Two::	DIELECTRIC LAYER FOR SHALLOW TRENCH ISOLATION
Formal Drawings?::	No
Application Type::	Utility
Docket Number::	016301-029900US
Secrecy Order in Patent Appl.?::	No

Prior Foreign Applications

Foreign Application One::	PCT/US00/22561
Filing Date::	16 August 2000
Country::	PCT
Priority Claimed::	Yes
Foreign Application Two::	EP99402068.3
Filing Date::	16 August 1999
Country::	Europe
Priority Claimed::	Yes